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Inventor Information for 10/563909

Inventor Name	City	State/Country
PIROVANO, MARCO	MILANO	ITALY
IMPROTA, STEFANIA	ROMA	ITALY
Appln Info Contents Petition Info [Atty/Agent-Info Continuity	/Reexam Foreign-Data Invent
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US	20060727	System of	604/65	•	Pirovano;
20060167404	20000727	infusion of	004/05		Marco et al.
		· · · · · · · · · · · · · · · · · · ·			Marco et al.
A1		pharmacological solutions			
110	20021210		275/149	275/150	Octor John
US	20031218	Wireless	375/148	375/150	Oates, John
20030231702		communications			H. et al.
A1		systems and			
		methods for			ı
		cache enabled			
		multiple			
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US	20031030	Wireless	375/148	375/150	Oates, John
20030202566		communications			H. et al.
A1		systems and			
	,	methods for		,	
		multiple			-
		processor based		i	
		multiple user	·		
		detection			
US .	20031030	Wireless	375/130		Oates, John
20030202559	20051050	communications			H. et al.
Al		systems and			
711		methods for			
		nonvolatile			
		storage of			
		operating			
		parameters for			,
		multiple			
		processor based			· ·
		multiple user	İ		
		detection		-	
US	20031023	Wireless	370/320	375/148	Oates, John
20030198197	20031023	communication	370/320	373/140	H. et al.
		1			11. 00 all.
A1.		systems and methods for		is:	
		contiguously			ļ
		addressable			
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	<u> </u>	memory enabled			
		multiple			
		processor based			
· ·		multiple user			
TIO	20021000	detection	711/104		Oates, John
US	20031009	Wireless	711/104	<u> </u>	Oales, John

20030191887	<u> </u>	communications			H. et al.
A1		l			11. Ct al.
AI		systems and methods for			
		direct memory			
		access and			
		buffering of			
		digital signals			
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US	20030710	Wireless	375/130		Oates, John
20030128739		communications			H. et al.
A1		systems and	•		·
		methods for			
		multiple	•		
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US 7110440	20060919	Wireless	375/148	375/144;	Oates; John
B2		communications		375/147	H. et al.
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US 7110437	20060919	Wireless	375/147	710/22;	Oates; John
B2		communications		711/212	H. et al.
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US 6526528	20030225	Ticket punch	714/56	714/51;	Imperiali;
B1		watchdog		714/55	Steven
		monitor			Robert
US 6463483	20021008	Low latency	710/35	710/22;	Imperiali;
B1		input-output		710/27;	Steven
		interface		710/305;	Robert
				710/306;	
				710/307;	
				710/308;	
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				710/312;	•
<u> </u>		J	1	1	

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